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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Robert F. Wallace et al.

Title: Use of Small Electronic Circuit Cards With Different Interfaces in an Electronic System

Application No.: 09/633,089 Filing Date: August 4, 2000

Examiner: Frech, Karl D. Group Art Unit: 2876

Docket No.: SNDK.149US0 Conf. No.: 1104

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants call the document listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application. A copy of the document listed on the accompanying Form PTO-1449 is enclosed.

Citation of this document shall not be construed as (1) an admission that the document is prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge

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any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664.

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Respectfully submitted,


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May 10, 2004

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